Inventor's Name: TAKAHASHI Application No.: New Application Docket No.: 100353-00179 REFERENCE CELL VX DECODER | CELL ARRAY |INPUT/OUTPUT | BUFFER Y DECODER DATA LATCH 4 CHIP-ENABLE/ OUTPUT-ENABLE CIRCUIT POTENTIAL SWITCHING CIRCUIT ADDRESS LATCH 의 CONTROL CIRCUIT ERASE CIRCUIT

" Title: NONVOLATILE SEMICONDUCTOR MEMORY DEVICE

STORING TWO-BIT INFORMATION

Title: NONVOLATILE SEMICONDUCTOR MEMORY DEVICE STORING TWO-BIT INFORMATION Inventor's Name: TAKAHASHI

Application No.: New Application Docket No.: 100353-00179

FIG.2



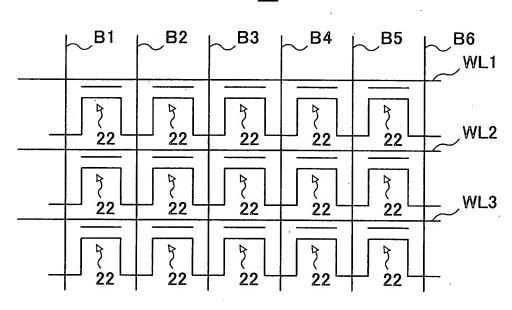
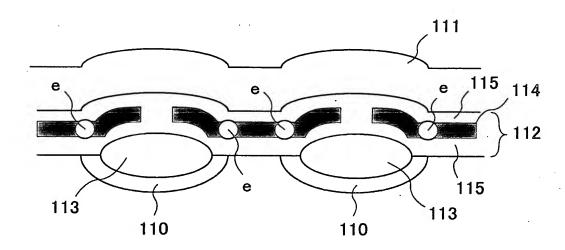


FIG.3



Title: NONVOLATILE SEMICONDUCTOR MEMORY DEVICE STORING TWO-BIT INFORMATION Inventor's Name: TAKAHASHI Application No.: New Application

FIG.4

Docket No.: 100353-00179

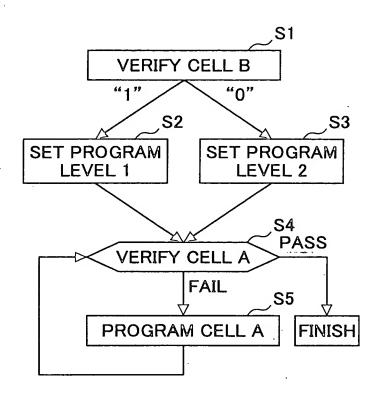
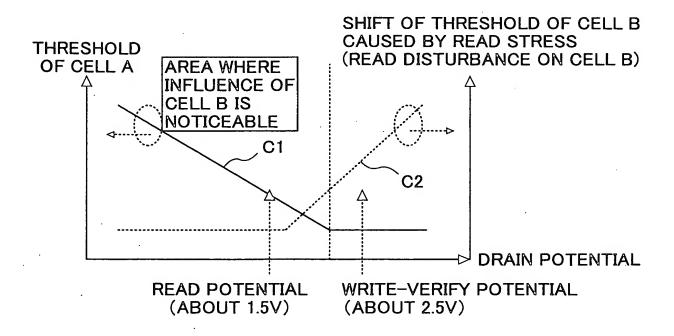


FIG.5



Title: NONVOLATILE SEMICONDUCTOR MEMORY DEVICE

STORING TWO-BIT INFORMATION

Inventor's Name: TAKAHASHI Application No.: New Application Docket No.: 100353-00179

FIG.6

